

Serial No.: 09/922,950  
Docket No.: A305-1  
AMA.040

### AMENDMENTS TO THE SPECIFICATION

The paragraph commencing at page 9, line 17 is amended as follows:

In the first memory map, the command RAM 12 and the external memory 15 are arranged in a memory space, as illustrated in FIG. 3. Specifically, the command RAM 12 is arranged in addresses of "0000000H" ~~to "0100000H", and to~~ "00FFFFFFH", and the external memory 15 is arranged in addresses of "0100000H" and later.

The paragraph commencing at page 10, line 12 is amended as follows:

When an interruption occurs, the central processing unit 2 branches to the interruption handling routine stored in the addresses of "0000000H" ~~to "0100000H", and to~~ "00FFFFFFH", and starts execution of the interruption handling routine. As illustrated in FIG. 3, the command RAM 12 is arranged in the addresses of "0000000H" ~~to "0100000H", and to~~ "00FFFFFFH", and hence, the central processing unit 2 reads a command out of the command RAM 12, and then, executes the interruption handling routine.

In the first memory map, the central processing unit 2 reads a reset routine out of the command RAM 12, and executes the reset routine, immediately after the central processing unit 2 has been reset.

The paragraph commencing at page 10, line 28 is amended as follows:

In the second memory map, the command RAM 12 and the external memory 15 are arranged in a memory space, as illustrated in FIG. 3. Specifically, the command RAM 12 is arranged in addresses of "0100000H" ~~to "0200000H", and to~~ "01FFFFFFH", and the external

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memory 15 is arranged in addresses of "0000000H" to ~~"0100000H"~~ and to "00FFFFFFH" and  
addresses of "0200000H" and later.